



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Confirmation No.: 5320

USHIYAMA et al. Examiner: T. Le

Serial No.: 10/694,997 Art Unit: 2818

Filed: October 29, 2003 Docket No. 100353-00176

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING REDUCED
CROSS-TALK NOISE

RESPONSIVE AMENDMENT UNDER 37 C.F.R. § 1.111

MAIL STOP AMENDMENT

December 10, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is filed in response to the Office Action mailed August 25, 2004, in the above-captioned patent application, the period for responding having been extended until December 27, 2004¹, by the attached Petition for a One-Month Extension of Time to Respond. In the prosecution of the above-captioned patent application, please consider the following:

¹ December 25, 2004, is a Saturday, and December 26, 2004, is a Sunday.